

LESSON PLAN

Name of the Faculty: Sangeeta
Discipline : Computer Science
Semester : 4th
Subject : Computer Architecture and Organisation
Lesson Plan Duration : 10 weeks



Work load (Lecture/Practical) per week : 3 lectures

Week	Theory		Practical	
	Lecture day	Topic	Practical day	Topic
1st	1	General register organisation, stack organisation		
	2	instruction formats (three address, two address, one address, zero address and RISC instruction).		
	3	Addressing modes: Immediate, register		
2nd	4	direct, in direct, relative, indexed		
	5	Memory Hierarchy, RAM and ROM chips		
	6	Memory address map, Memory connections to CPU		
3rd	7	Auxiliary memory: Magnetic disks		
	8	magnetic tapes, Associative memory		
	9	Cache memory		
4th	10	Virtual memory		
	11	Memory management hardware		
	12	Read and Write operation		
5th	13	Basis Input output system (BIOS) - Function of BIOS		
	14	Testing and initialization		
	15	Modes of Data Transfer, Programmed I/O: Synchronous		
6th	16	asynchronous and interrupt initiated. DMA data transfer		
	17	Forms of parallel processing,		
	18	Parallel processing and pipelines		
7th	19	basic characteristics of multiprocessor		
	20	General purpose multiprocessors		
	21	Interconnection networks: time shared common bus, multi-port memory, cross bar switch,		
8th	22	multi stage switching networks		
	23	hyper cube structures.		
	24	Define I/O interface		
9th	25	Input-Output Interface		
	26	Explain methods of Asynchronous Data transfer.		
	27	Synchronous Data Transfer		
10th	28	Strobe Control		
	29	Handshaking		
	30	Describe Asynchronous Serial Transfer.		